

## IN THE SPECIFICATION

On page 1 of the Specification, please amend the first paragraph as follows:

### CROSS REFERENCE TO RELATED APPLICATIONS

This application is a continuation of co-pending commonly owned US patent application number 10/390,935 filed on March 18, 2003, entitled "Power Amplifier Circuitry And Method", which is a continuation of US patent application number 09/660,123 filed on September 12, 2000 (US Patent 6,549,071), entitled "Power Amplifier Circuitry And Method". The following U. S. patent applications filed concurrently herewith, which were filed concurrently with the first parent application: Serial No. 09/660,009[\_\_\_\_\_]], entitled "RF Power Amplifier Circuitry and Method for Amplifying RF Signals" by Timothy J. Dupuis et al (US Patent 6,462,620); Serial No. 09/659,876[\_\_\_\_\_]], entitled "Method and Apparatus for Regulating a Voltage" by Timothy J. Dupuis et al (US Patent 6,362,606); Serial No. 09/659,636[\_\_\_\_\_]], entitled "Dual Oxide Gate Device and Method for Providing the Same" by Timothy J. Dupuis et al (US Patent 6,392,488); Serial No. 09/659,874[\_\_\_\_\_]], entitled "RF Power Amplifier Device and Method for Packaging the Same" by Timothy J. Dupuis et al (abandoned); and Serial No. 09/659,873[\_\_\_\_\_]], entitled "Apparatus and Method for Providing Differential-to-Single Ended Output Conversion and Impedance Transformation" by Susanne A. Paul et al (US Patent 6,448,847), are expressly incorporated herein by reference.